ABSTRACT

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A plasma display panel in which a plurality of pairs of first and second electrodes are disposed on a first substrate so as to be parallel to each other, a plurality of third electrodes are disposed on a second substrate, and main parts of a plurality of barrier ribs are disposed between adjacent third electrodes, the third electrodes being orthogonal to a longitudinal direction of display electrodes each of which consists of a pair of the first and second electrodes, wherein a plurality of fourth electrodes are fixed to the barrier ribs or areas of a surface of the first substrate facing the barrier ribs so as to be at least invicinities of areas between adjacent display electrodes, the fourth electrodes being electrically exposed to discharge spaces which are defined by the barrier ribs.